

METHOD AND APPARATUS FOR DETERMINING A
PROCESSING SPEED OF AN INTEGRATED CIRCUIT
Inventor: Greg Sadowski; Atty. Docket No. 00100.03.0011

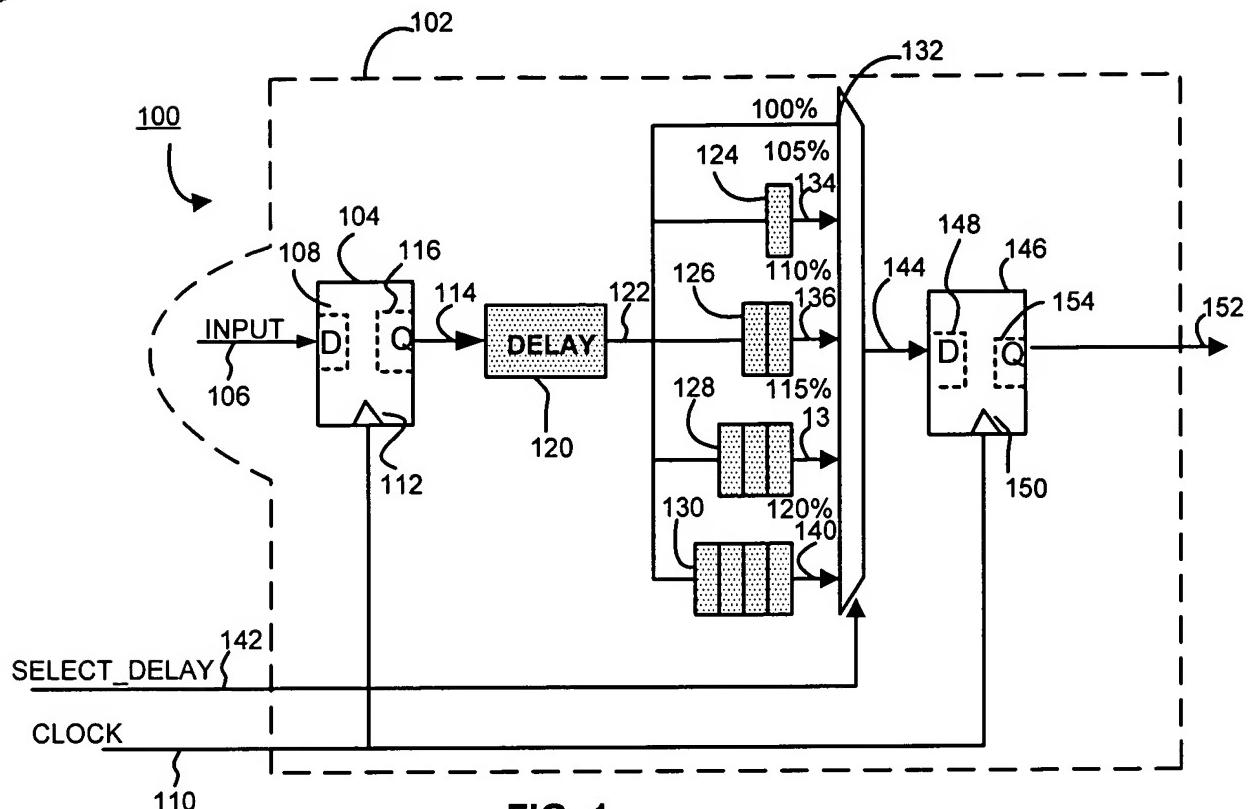


FIG. 1

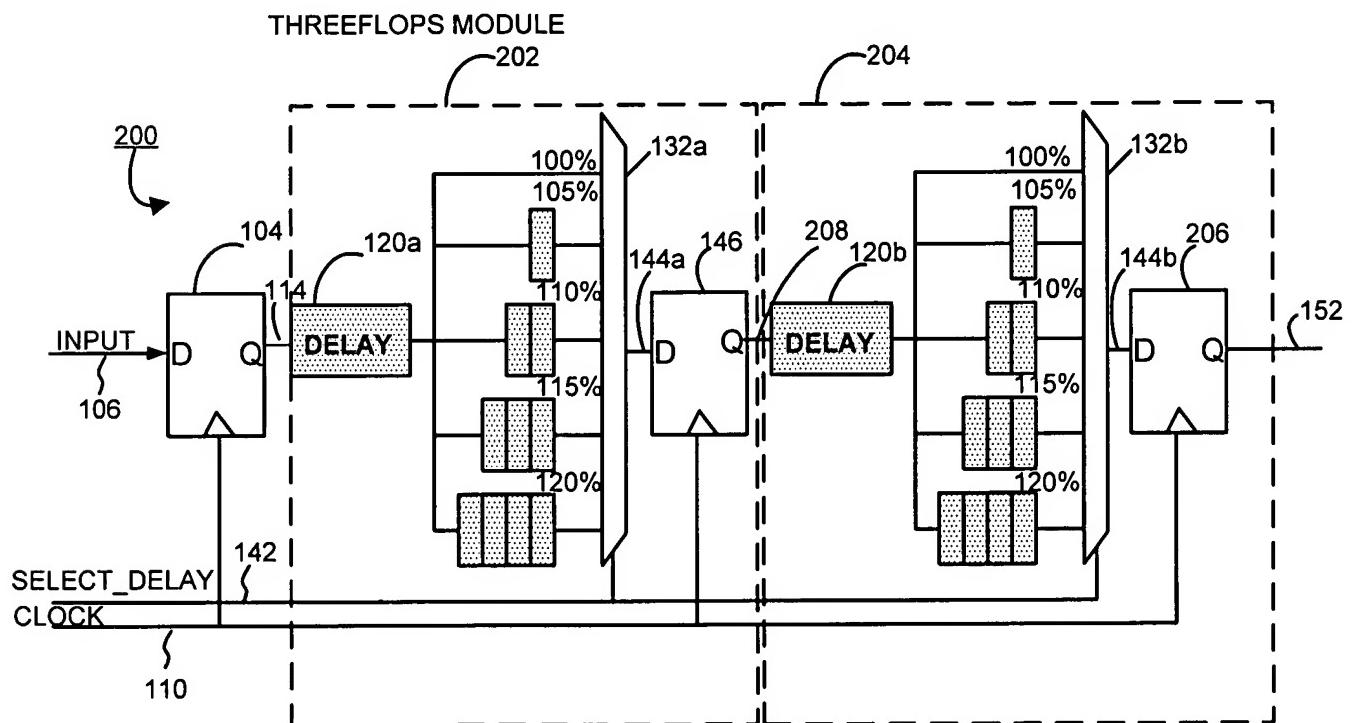


FIG. 2

METHOD AND APPARATUS FOR DETERMINING A
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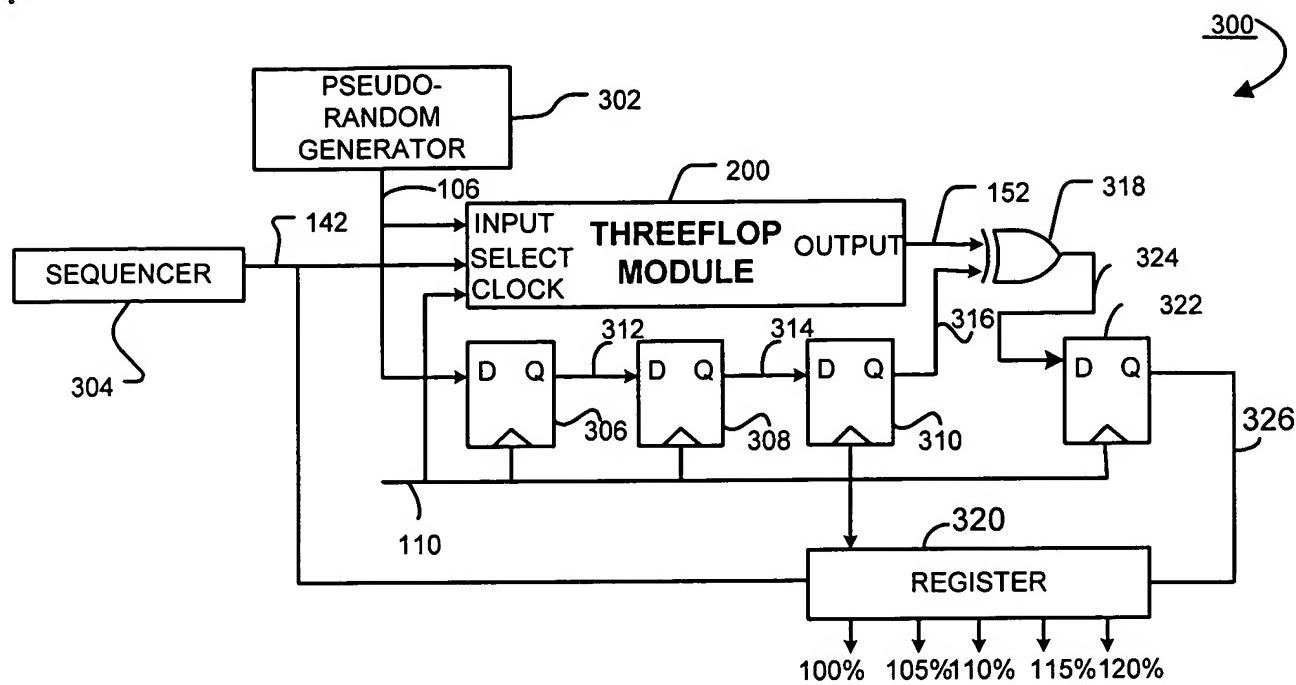


FIG. 3

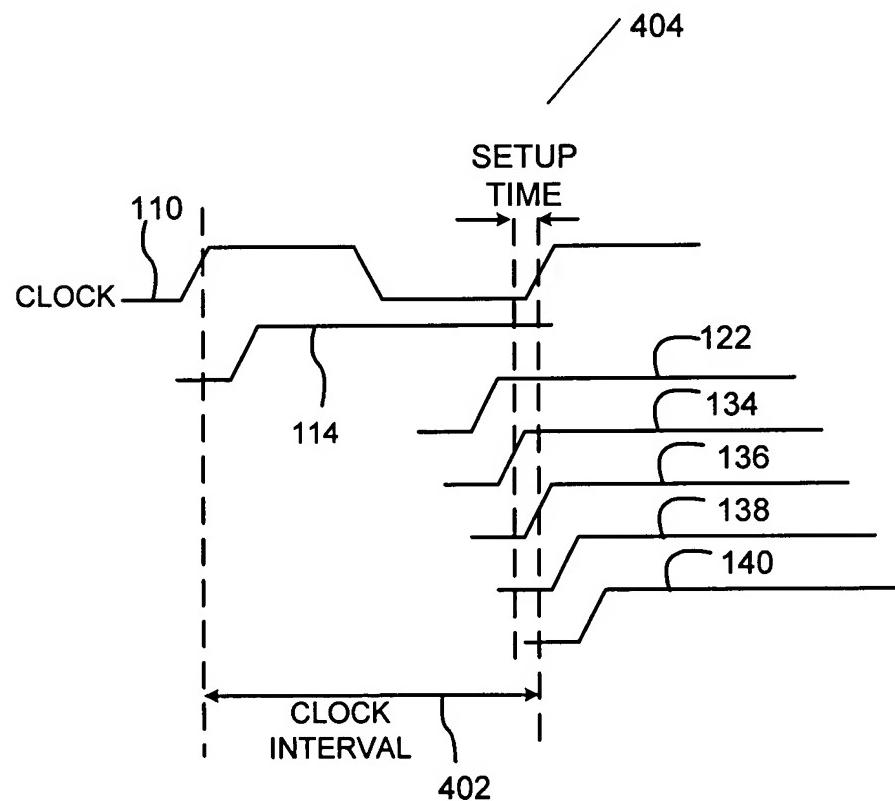


FIG. 4

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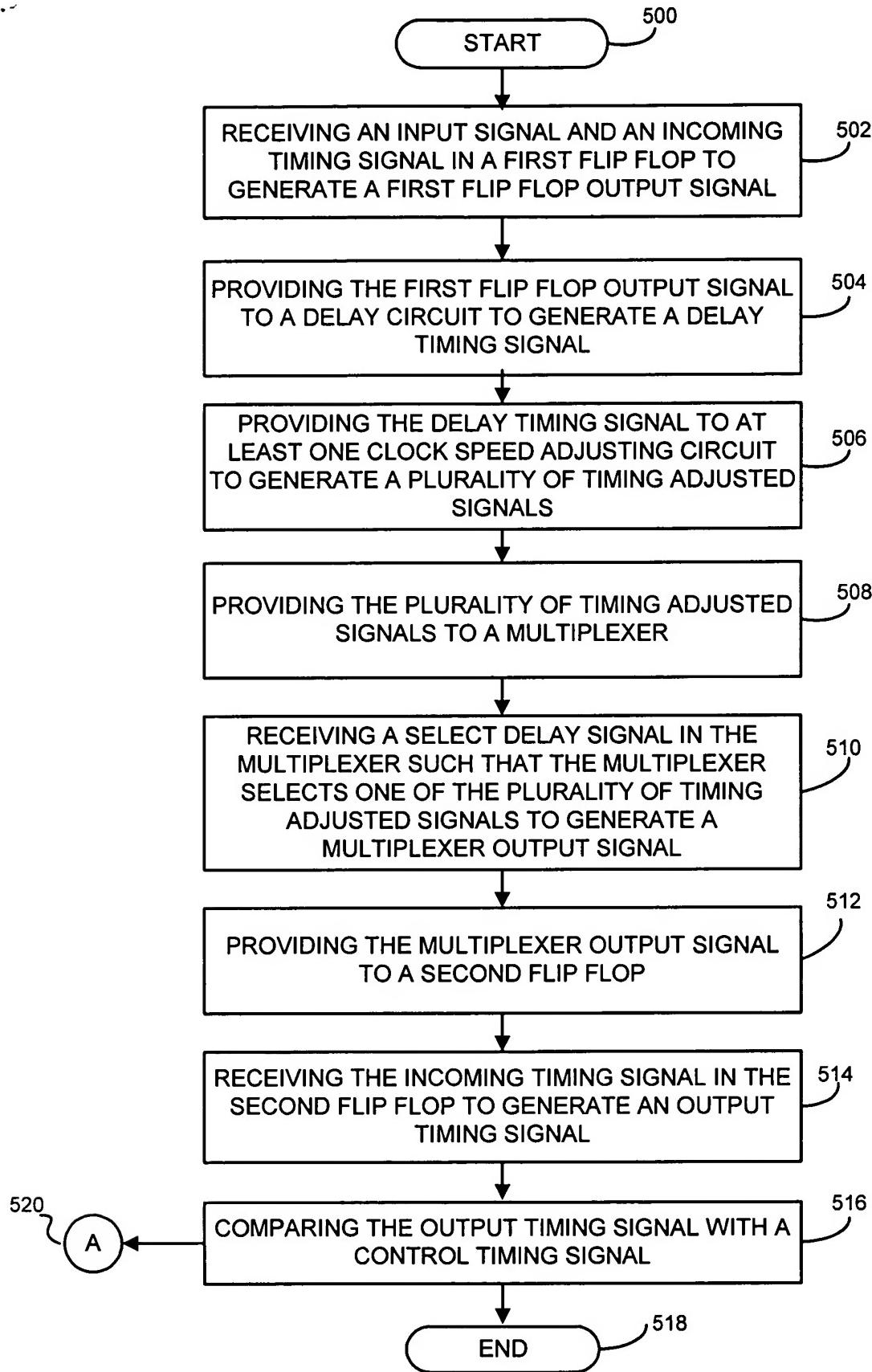


FIG. 5

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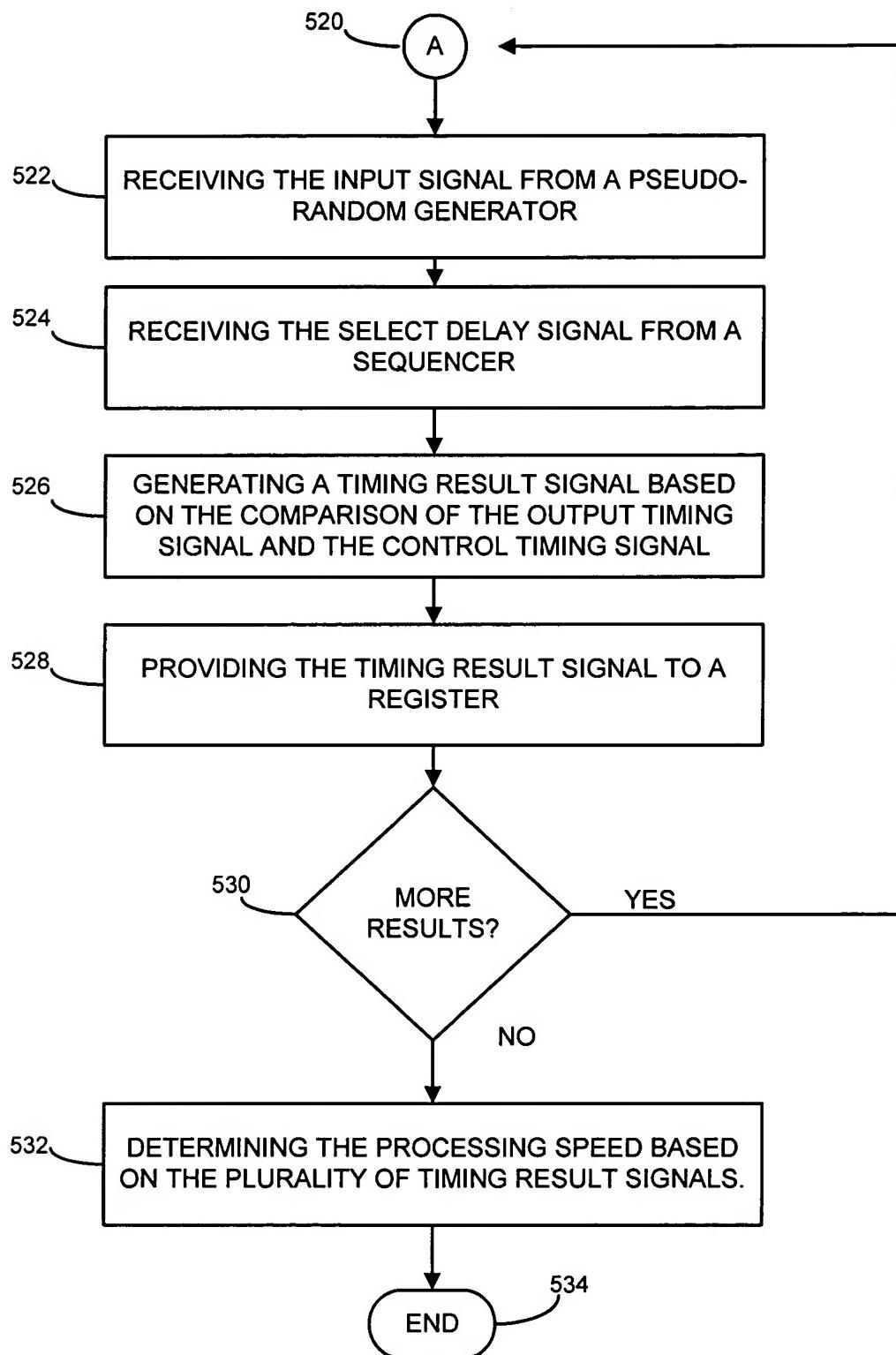


FIG. 6

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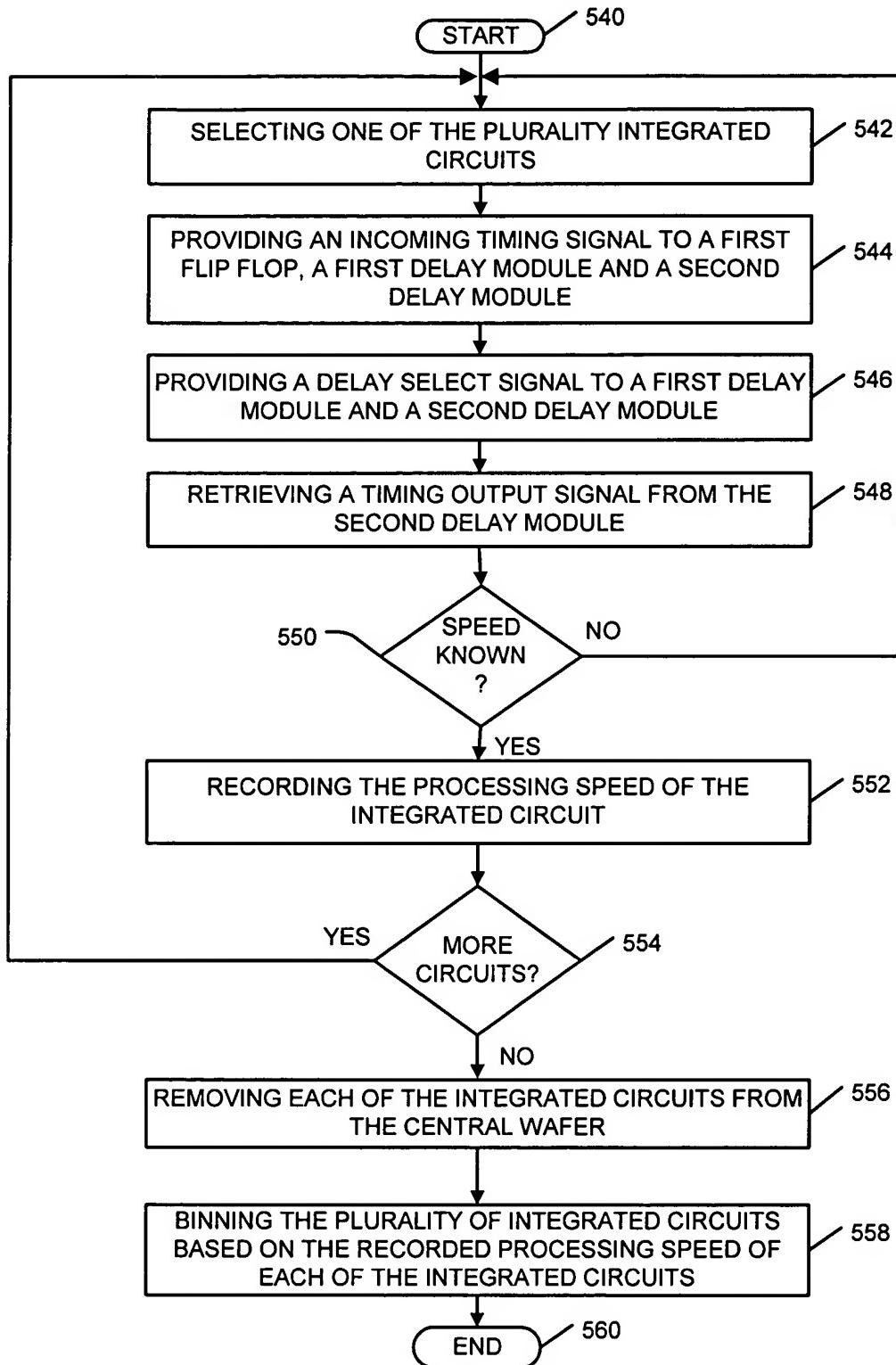


FIG. 7